



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Kerry Berland, et al. ATTY. DOCKET NO. 09771110-0010
SERIAL NO. 10/821,551 GROUP ART UNIT: 2862
FILING DATE: April 8, 2004 EXAMINER: Jay M. Patidar
INVENTION: MULTI-TEST CIRCUIT INTERRUPTER LOCATOR AND CIRCUIT
INTERRUPTER TESTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

SUBMISSION OF CORRECTED DRAWINGS

Please replace the drawing sheet for Figure 10 with the attached replacement drawing sheet.

Respectfully submitted,

By: Christopher P. Rauch Reg. 45,034
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